

Errata

GPU-based Architecture Modeling and  
Instruction Set Extension for Signal Processing  
Applications

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- **Page 42:** Figure 4.3 missing a note. “First published in the Proceedings of the 27th European Signal Processing Conference (EUSIPCO-2019) in 2019, published by EURASIP”
- **Page 46:** Figure 4.6 missing a note. ”First published in the Design Automation for Embedded Systems 2021”